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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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09/725,569

11/30/2000

Yoshiyuki Namizuka

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07/06/2004

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EXAMINER

GRANT II, JEROME

ART UNIT

PAPER NUMBER

2626

DATE MAILED: 07/06/2004

20

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	09/725,569	NAMIZUKA ET AL.	
	Examiner	Art Unit	
	Jerome Grant II	2626	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☐ Claim(s) ____ is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-14 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

JEROME GRANT II
PRIMARY EXAMINER

Attachment(s)

- | | |
|---|--|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>20</u> . | 6) <input type="checkbox"/> Other: ____ |

Detailed Action

1.

Rejections Under Section 112

Regarding claims 1-6 and 8-13, the phrase "or the like" renders the claim(s) indefinite because the claim(s) include(s) elements not actually disclosed (those encompassed by "or the like"), thereby rendering the scope of the claim(s) unascertainable. See MPEP § 2173.05(d). See line 7 of claims 1 and 8.

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-4, 6-11, 13 and 14 are rejected under 35 U.S.C. 102(b) as being anticipated by Matsuda.

With respect to claim 1, Matsuda teaches an image processor (still unit picture transmitter, comprising: an image memory 8 (see line 3 of the purpose); an image memory control unit 9 which is connected to an image reading unit for reading image data and image processing unit 3, 10 + 12 for processing and editing image data and image writing unit 4 for writing image data to transfer paper. Matsuda teaches a LPF2 which receives first image data read in by said image reading unit (camera, not shown, but see line 2 of the Purpose) second image data (output from memory 8) subjected to image processing by said image processing unit (10 and 12); a system control unit 6 which controls transmission or reception of control signals (transmission by modem 13 and reception by switcher 1. Matsuda teaches a source detection unit (sync separator 3) which detects a source of image data (still or television images) to said image memory control unit (see col. 2); wherein said system control unit 6 controls said image memory control unit 9 according to the source (camera inputs which are still images or television images according to col. 2, see also the sync. Separator 3), of the image data detected by said source detection unit, and determines a transmission order (see first tow lines of the Constitution and the first paragraph. of col. 5).

With respect to claims 2 and 9, Matsuda teaches an image processor wherein said image memory control unit 9 is connected to said one or more units 3 through an image data control unit 6, wherein said image data control unit 6 performs transmission or reception of image data between said image memory control unit 9 and one or more units, see figure 1.

With respect to claims 3 and 10, Matsuda teaches image memory 8, said image control unit 9, and system control unit 6 formed as a discrete control unit. See figure 1.

With respect to claims 4 and 11, Matsuda teaches the claimed limitation. See the big arrow labeled bus in figure 1 between elements 8 and 9.

With respect to claims 6 and 13, Matsuda teaches an image expansion unit (modem 13) for expanding image data; and a compression determination unit (modem 13) for determining compressed data and memory control unit 9 for controlling the transmission of expanded and compressed data from modem 13.

With respect to claim 7, Matsuda teaches an image processor according to figure 1, comprising: an image reading unit (camera, line 2 of the Purpose); image processing unit via an image processor (still unit picture transmitter; : an image memory 8 (see line 3 of the purpose); an image memory control unit 9 which is connected to an image reading unit for reading image data and image processing unit 3, 10 + 12 for processing and editing image data and image writing unit 4 for writing image data to transfer paper. Matsuda teaches an image memory control unit 9, which receives data from a camera units 1-4 or the image processing via the image processing unit (10 and 12); data is taken from the memory 8 and is transmitted to processing units 10 and 12.. Matsuda

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teaches a system control unit 6 which controls transmission or reception of control signals (transmission by modem 13 and reception by switcher 1. Matsuda teaches a source detection unit (sync separator 3) which detects a source of image data (still or television images) to said image memory control unit (see col. 2); wherein said system control unit 6 controls said image memory control unit 9 according to the source (camera inputs which are still images or television images according to col. 2, see also the sync. Separator 3), of the image data detected by said source detection unit, and determines a transmission order (see first two lines of the Constitution and the first paragraph. of col. 5).

With respect to claim 8, Matsuda teaches an image processor (still unit picture transmitter, comprising: an image memory 8 (see line 3 of the purpose); an image memory control unit 9 which is connected to an image reading unit for reading image data and image processing unit 3, 10 + 12 for processing and editing image data and image writing unit 4 for writing image data to transfer paper. Matsuda teaches a controller 6 which receives as well as LPF2 which receives first image data read in by said image reading unit (camera, not shown, but see line 2 of the Purpose); second image data (output from memory 8) subjected to image processing by said image processing unit (10 and 12); a system control unit 6 which controls transmission or reception of control signals (transmission by modem 13 and reception by switcher 1. Matsuda teaches a source detection unit (sync separator 3) which detects a source of

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image data (still or television images) to said image memory control unit (see col. 2); wherein said system control unit 6 controls said image memory control unit 9 according to the source (camera inputs which are still images or television images according to col. 2, see also the sync. Separator 3), of the image data detected by said source detection unit, and determines a transmission order (see first tow lines of the Constitution and the first paragraph. of col. 5).

With respect to claim 14, Matsuda teaches an image processor (still image transmitter of figure 1) comprising:

An image reading means (camera means outputting input values I-IV) for acquiring image data; an image processing means (entire right portion of the circuit of figure 1 from switch 1) for processing the image data acquired by said image reading means; an image memory 8 for storing the image data acquired by said image reading means for the image data processed by said image processing means; an image memory control means 9 for receiving data sent from said image reading means (camera with inputs 1-4) and receives the data sent from the image memory 8 and transmits it to said image reading means or image processing means 10 + 12; a system control means 6 for controlling transmission or reception of data by said image memory control means; and a detection means 3 for detecting which one of said image reading means (inputs 1-4) has transmitted data to the image control means 9. wherein said control means 6 controls said image memory control means 9 based on the detected source 3 (whether still or television images have been detected) and determines an order in which the

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image data is to be transmitted to said image memory, see the two lines of the Constitution and paragraph 1 of col. 5.

Claims Objected As Containing Allowable Matter

2.

Claims 5 and 12 would be allowable if rewritten to overcome the rejection(s) under 35 U.S.C. 112, second paragraph, set forth in this Office action and to include all of the limitations of the base claim and any intervening claims.

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jerome Grant II whose telephone number is 703-305-4391. The examiner can normally be reached on Mon.-Fr. from 9:00 to 5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kimberly A Williams, can be reached on 703-305-4863. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR.

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Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).



J. Grant II

JEROME GRANT II
PRIMARY EXAMINER